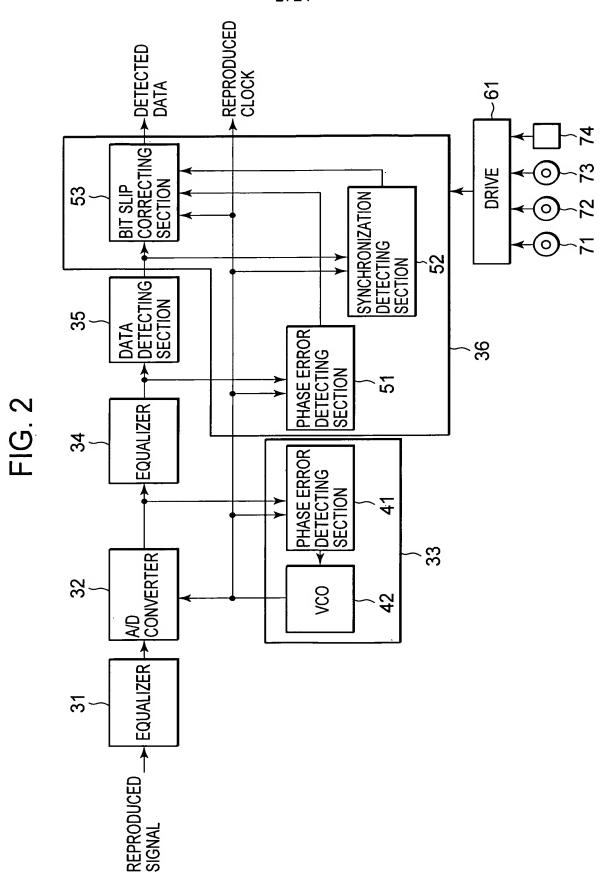


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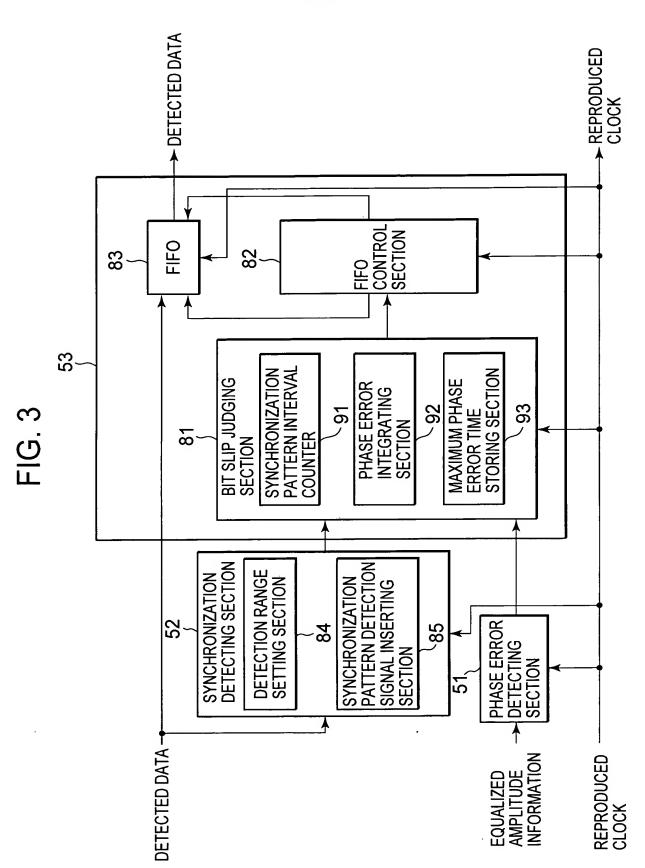
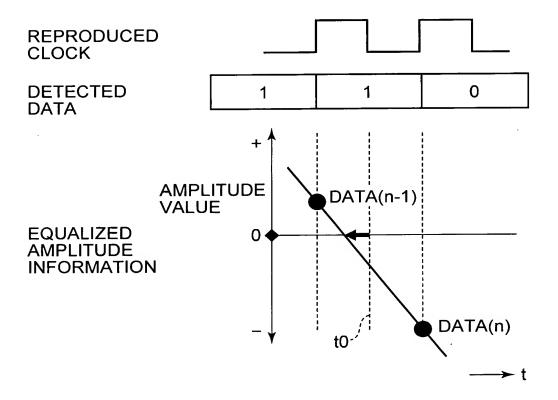


FIG. 4



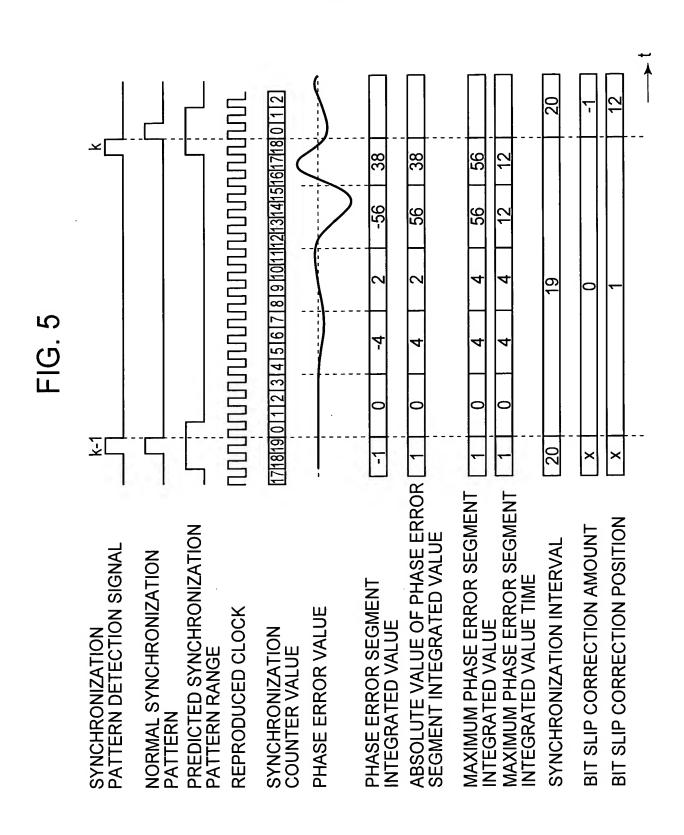
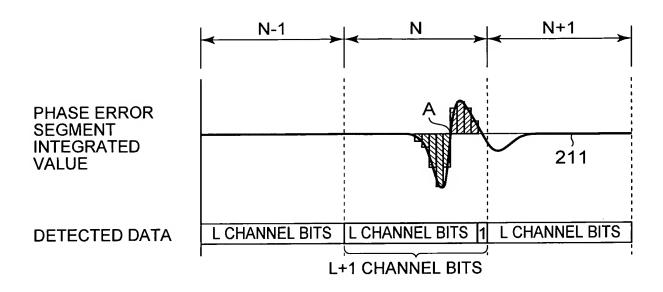


FIG. 6



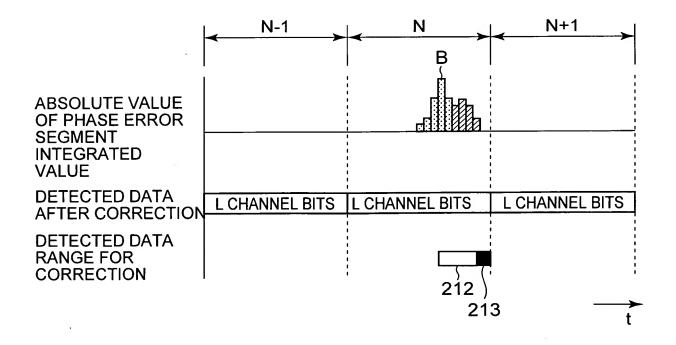


FIG. 7

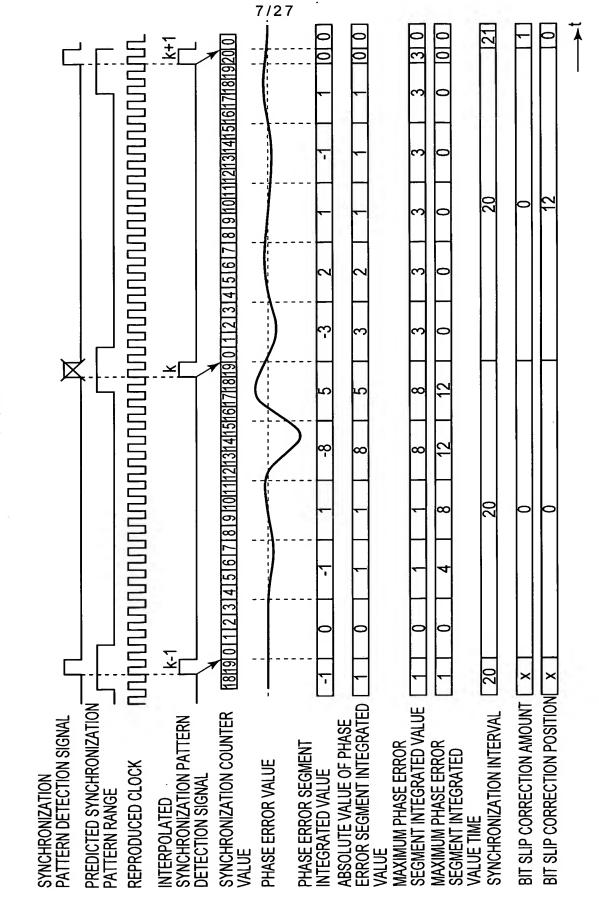
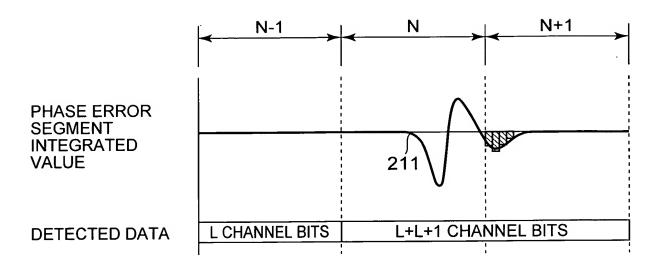


FIG. 8



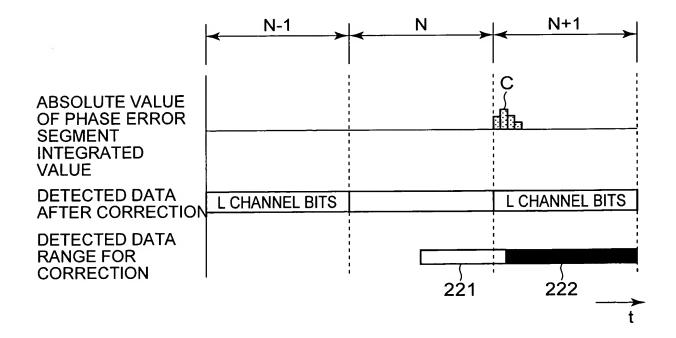


FIG. 9

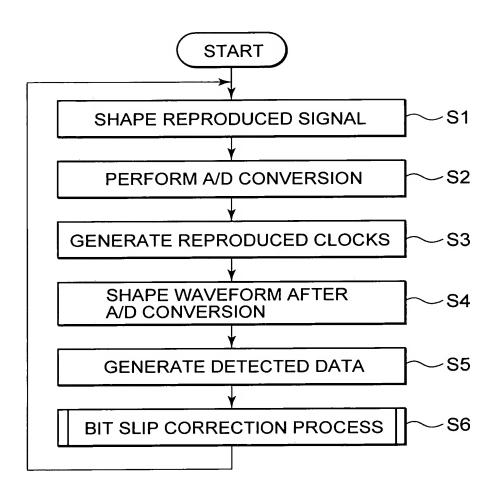


FIG. 10

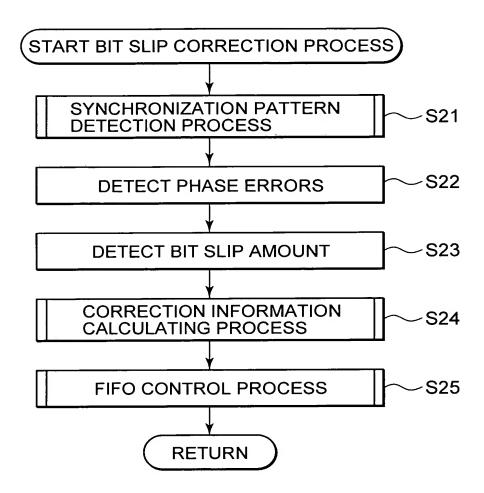


FIG. 11

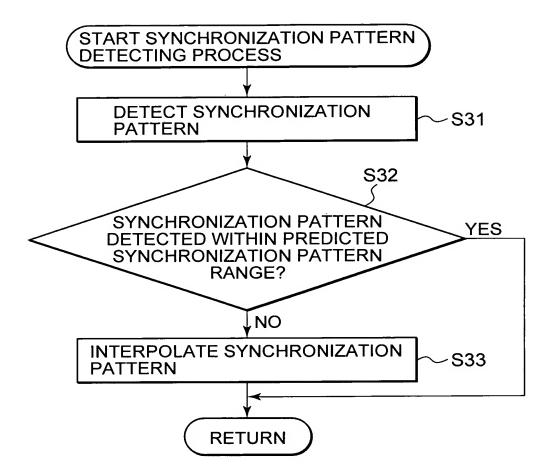


FIG. 12

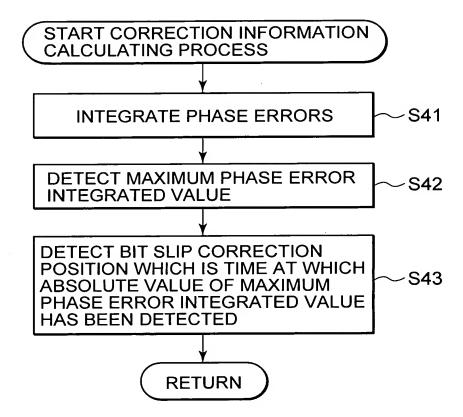
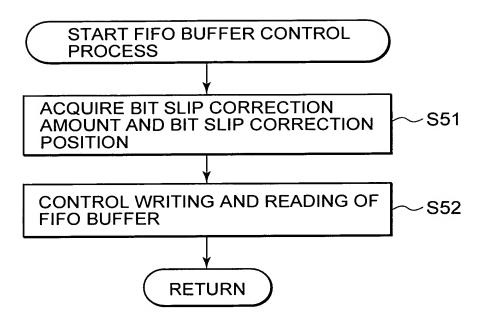


FIG. 13



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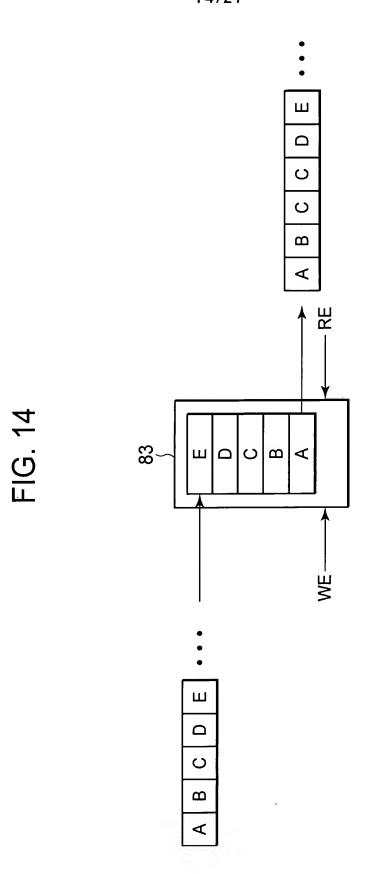
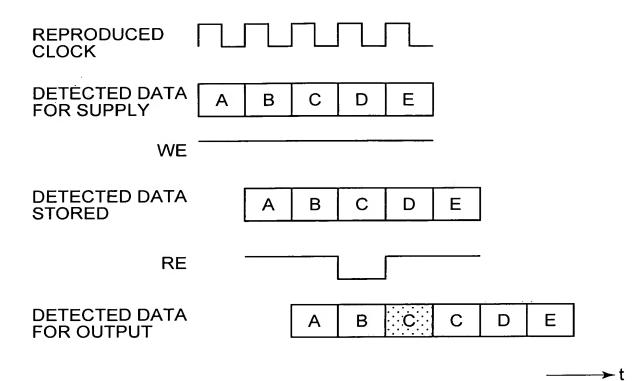


FIG. 15



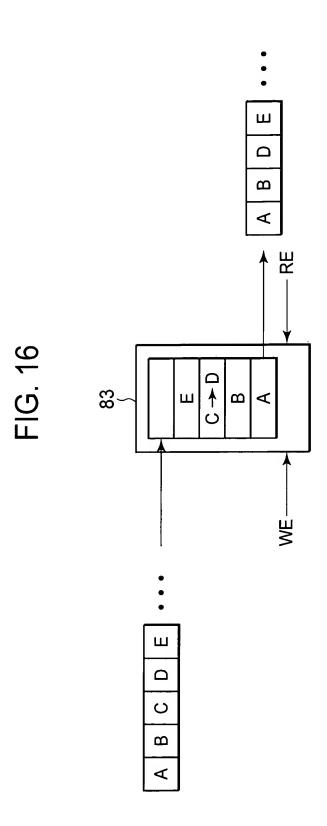
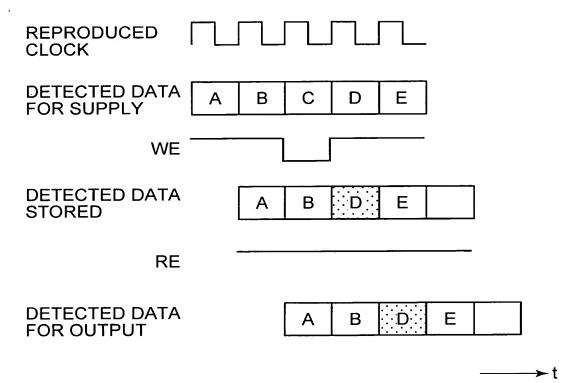
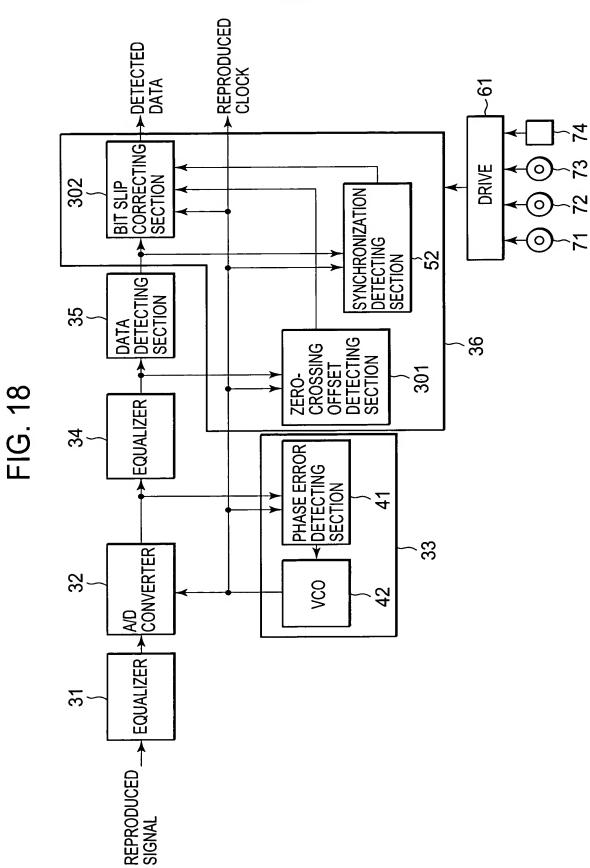


FIG. 17



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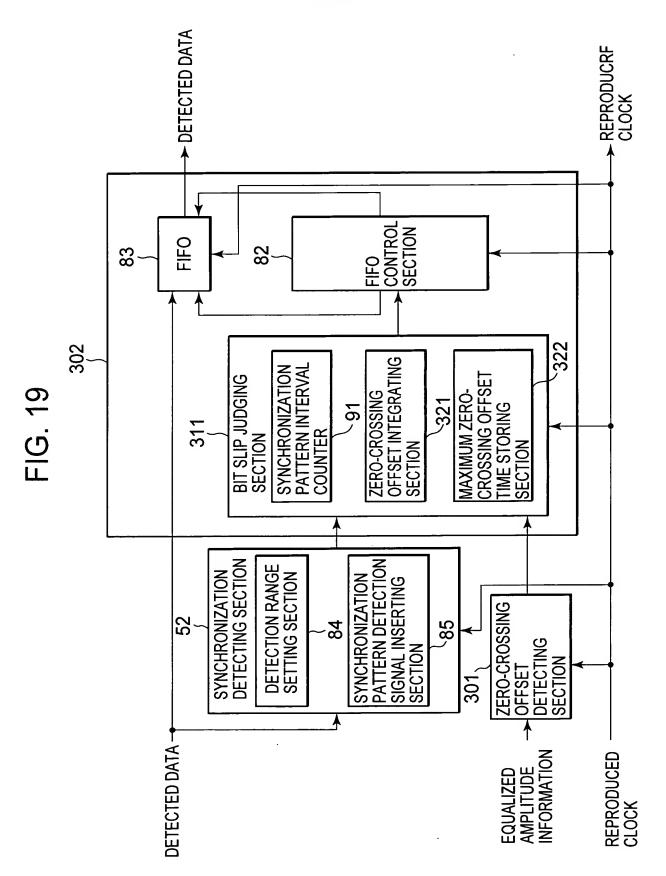
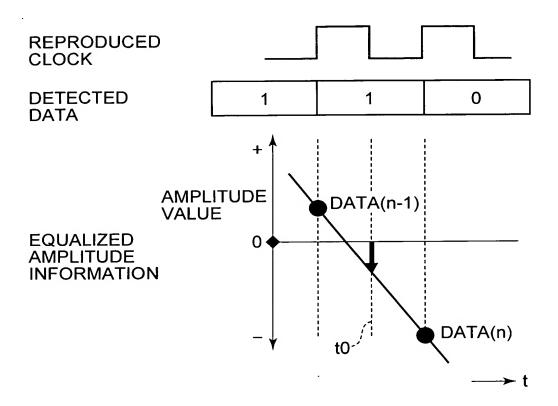
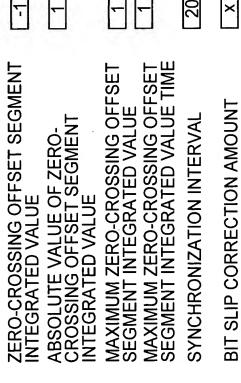


FIG. 20



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BIT SLIP CORRECTION POSITION

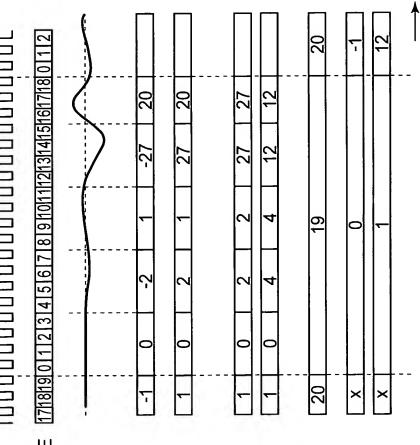
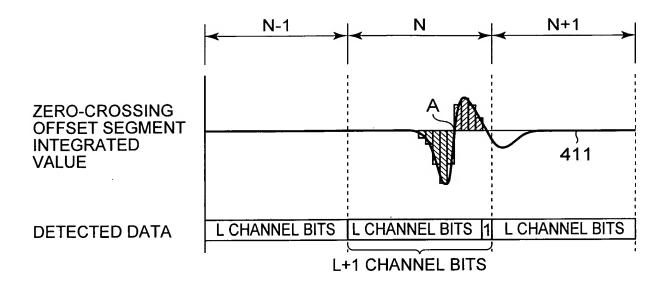
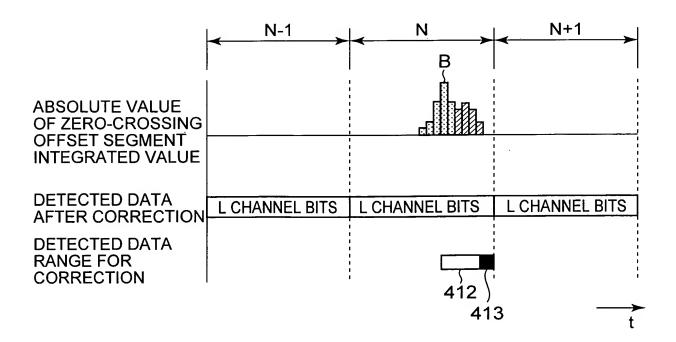


FIG. 22





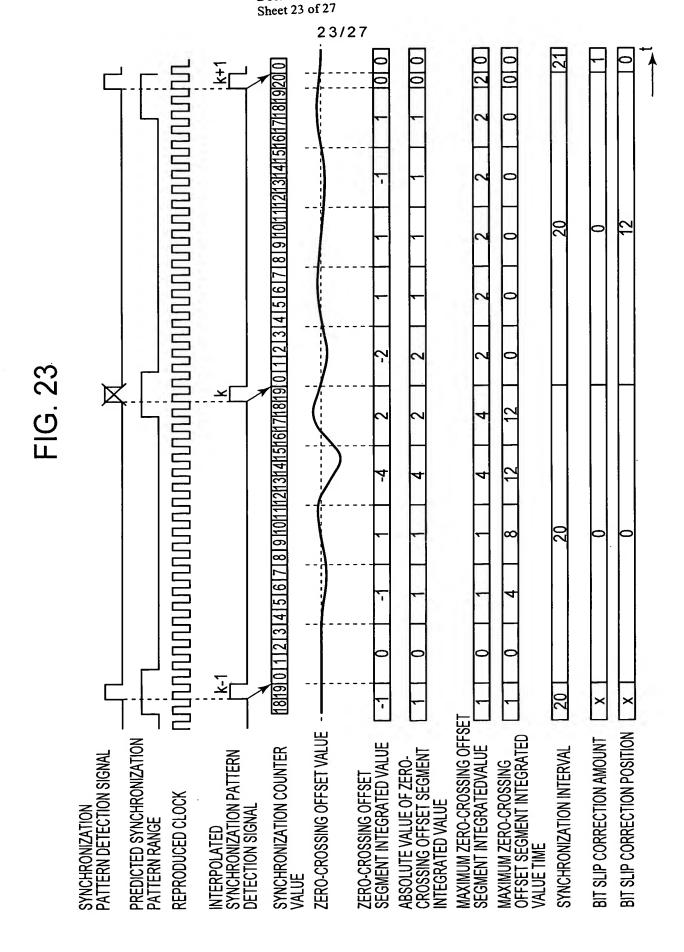
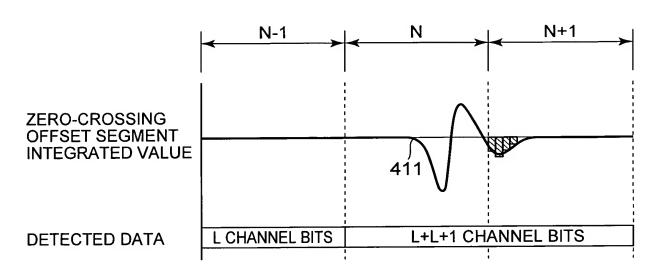


FIG. 24



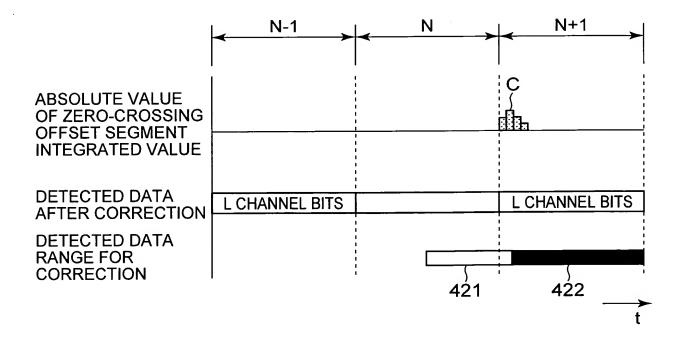


FIG. 25

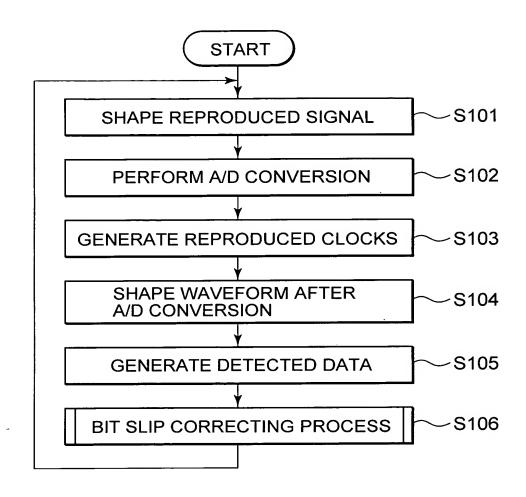


FIG. 26

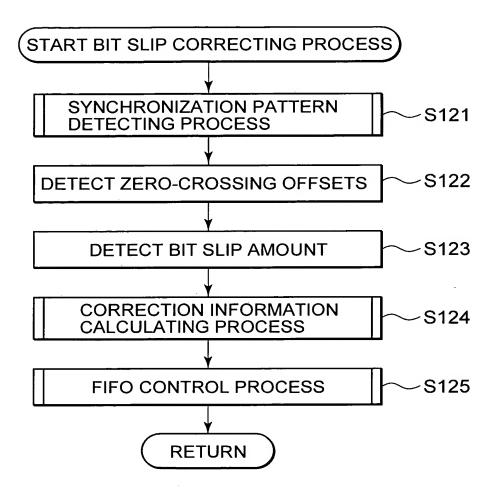


FIG. 27

